Contents lists available at ScienceDirect

# Thin Solid Films

journal homepage: www.elsevier.com/locate/tsf

## Effects of low-temperature annealing on electrical properties of Thin-film Transistors based on Zinc Oxide films deposited by ultrasonic spray pyrolysis: Impact of annealing time

Miguel A. Dominguez<sup>a,\*</sup>, Francisco Flores<sup>a</sup>, Javier Martinez<sup>a</sup>, Abdu Orduña-Diaz<sup>b</sup>

<sup>a</sup> Centro de Investigaciones en Dispositivos Semiconductores, Instituto de Ciencias, Benemerita Universidad Autonoma de Puebla (BUAP), Puebla 72570, Mexico <sup>b</sup> Centro de Investigacion en Biotecnologia Aplicada (CIBA), Instituto Politecnico Nacional, Tlaxcala, Tlaxcala 72197, Mexico

#### ARTICLE INFO

Article history: Received 4 March 2016 Received in revised form 13 July 2016 Accepted 15 July 2016 Available online 17 July 2016

Keywords: ZnO Thin-film transistors X-ray diffraction Electrical properties

#### 1. Introduction

Currently, Zinc Oxide films (ZnO) have a great application in potential low-cost devices, such as solar cells and Thin-film Transistors (TFTs) [1–6]. In these devices are desirable to have a low contact resistance for an optimum electrical performance. In an ideal metal-semiconductor contact, there are no barriers for the carrier flow in either positive or negative polarization. Ideally, this occurs when metal and semiconductor work functions are of similar value and there are no interface states. However, usually, having a metal-ZnO contact without interface states is difficult. Also, matching the semiconductor and metal work functions is nearly impossible [7]. It is important to say that contact metallization technology in ZnO has not been explored extensively [7]. Some authors have attempted to reduce the contact resistance in metal-ZnO interfaces [8–16]. Moreover, different thermal treatments have been reported for ZnO alloys and TFTs [17-31]. However, it is well-known, that ZnO films are highly dependent on the deposition and post-treatments conditions, resulting in different surface conditions and defect density distribution. For this reason, different results have been reported. In [8], contacts annealed at 300 °C resulted in a reduction by two orders of magnitude in contact resistance; however, annealing at 500 °C resulted in a reduction of one order of magnitude only. Also, in other works, have been reported that metal-ZnO contact deteriorates after annealing at higher temperature, where the contact resistance is increased [9,16]. Contrary to these results, in [15] reported Ru-ZnO contacts annealed at

\* Corresponding author. *E-mail address:* madominguezj@gmail.com (M.A. Dominguez).

### ABSTRACT

In this work, the study of annealing effects on electrical properties of Zinc Oxide Thin-film Transistors is presented. The samples were annealed at 180 °C under Nitrogen ambient. The conductivity and contact resistance of ZnO films with Aluminum electrodes are studied by Transmission Line Method. Also, the Zinc Oxide films obtained by ultrasonic spray pyrolysis at 200 °C are studied by X-ray diffraction and Fourier transform infrared spectroscopy. A comparison of the electrical properties as a function of annealing time is presented. The results show an optimal annealing time and after this time, the metal-ZnO interface deteriorates.

© 2016 Elsevier B.V. All rights reserved.

700 °C without any electrical degradation. Therefore, a quantitative comparison of the contact resistance would be unfeasible, because it depends on the quality of the ZnO film and metal contact used.

On the other hand, although the improvement of electrical characteristics of ZnO TFTs by post-deposition thermal annealing has already been demonstrated, the temperature of post-deposition treatments to improve performance is too high [23–31]. Since the aim of Oxide TFTs development is to be an alternative to the typical a-Si TFTs, which are fabricated at temperatures close to 200 °C [32–35], then it is necessary to keep the complete fabrication temperature at low values, less than 200 °C, in order to be a real alternative to a-Si for large-area and flexible applications [35].

In this work, the study of annealing effects on electrical properties of ZnO Thin-film Transistors is presented. The ZnO films were deposited by ultrasonic spray pyrolysis at 200 °C. The samples were annealed at 180 °C under Nitrogen ambient. The conductivity and contact resistance of the ZnO films with Aluminum (Al) electrodes are studied by Transmission Line Method (TLM), a comparison of the electrical properties as a function of annealing time is presented. Also, the ZnO films are studied by X-ray diffraction (XRD) and Fourier transform infrared spectroscopy (FTIR). It is important to note, as far as the authors know, low-temperature annealing effects on ZnO deposited by ultrasonic spray pyrolysis have not been published yet.

#### 2. Experiment

The ZnO thin films were deposited using a home-made ultrasonic spray pyrolysis deposition system adapted from an ultrasonic





humidifier (Heaven Fresh), from 0.2 M precursor solution of Zinc acetate in methanol, using air as carrier gas at flow rate of 467 sccm. The substrate was placed on a hot-plate at 200 °C during deposition. The nozzle is located at 8 cm-height to cover a substrate area of 5 cm  $\times$  5 cm. The post-deposition annealing was conducted in a furnace with Nitrogen atmosphere at 180 °C for different annealing times: 15, 30, 45 and 60 min.

The Al-ZnO contacts were characterized by TLM, which consists in current–voltage measurements at different contact separations [36]. The contact separation was 5, 10, 25, 30 and 35 µm, and the contact width was 120 µm. The resistance measured between the contacts is described by:

$$\mathbf{R}_{\mathrm{T}} = \rho \mathrm{sd}/\mathrm{Z} + 2\mathrm{R}_{\mathrm{C}} \tag{1}$$

where  $\rho$ s is the resistivity, d is the contact separation, Z is the contact width and R<sub>c</sub> is the contact resistance. To calculate the contact resistance, the interception at d = 0 in the plot of resistance as a function of d gives the value of 2R<sub>c</sub>.

The orientation of the film was investigated using X-ray diffractometer (XRD) (Discover D8-Bruker axs) at 20 range between  $10^{\circ}$ and  $90^{\circ}$  and  $0.002^{\circ}$  step. The IR absorption spectra was measured with a "BRUCKER" FTIR spectrometer, Model Vertex-70.

The inverted coplanar ZnO TFTs (bottom-contact bottom-gate) were made of 35 nm-thick ZnO deposited over 100 nm-thick Aluminum electrodes patterned on 50 nm-thick thermally grown  $SiO_2$  on heavily-doped Si wafers [4,5]. Better electrical performance in TFTs using thinner active layer has been reported, for this reason, active layer thickness of 35 nm was used [37,38]. The thickness was measured by ellipsometry and confirmed by measuring an etched step. The electrical characteristics were measured using the Keithley-4200 Semiconductor Characterization System at room temperature and under dark conditions.

#### 3. Results and discussion

Fig. 1 shows the contact resistance and conductivity of Al-ZnO contacts annealed at 180 °C as a function of annealing time. The figure shows a reduction of two orders of magnitude in contact resistance for samples annealed for 30 min. This improvement is related to a higher carrier injection through the Al-ZnO interface. P. Nunes et al. [39–41] found that this is due to desorption of oxygen present at the grain boundaries of the ZnO film. This results in an increase of the effective carrier concentration by the oxygen loss near to the Al-ZnO interface [7,10–12]. The improvement of two orders of magnitude in contact resistance is similar than that previously reported by other authors [8, 13–16]. In [8], the annealing at 300 °C for 1 min under Nitrogen ambient of as-deposited contacts resulted in a reduction of two orders of magnitude in contact resistance; however, the annealing at 500 °C resulted in a reduction of one order of magnitude only.

On the other hand, to improve metal-ZnO contacts, a specific annealing time is used without any explanation [8,9,15,16,18–21,25–29]. Only few authors reported the use of different annealing times [30,31]. For this reason, in order to study the impact of annealing time, a longer annealing time is explored. Longer annealing than 30 min results in an increase of the contact resistance, as shown in Fig. 1. This is due to a change of the quality in the ZnO film, as can be interpreted with the reduction of conductivity. These results show that there is an optimal annealing time and after this time, the metal-ZnO interface deteriorates. In order to address this assumption, X-ray diffraction and FTIR spectroscopy were used to study the effects of annealing time on ZnO films.

Fig. 2 shows the FTIR spectra of ZnO films at different annealing times. It can be seen the peak at 415  $\text{cm}^{-1}$  related to Zn—O stretching modes [42,43]. The characteristic Zn—O band indicates the ZnO formation. The figure clearly shows a reduction in Zn—O bonds as the annealing time is increased. This can be correlated with the results of conductivity and contact resistance, where the oxygen vacancies increase the carrier concentration. However, at 60 min of annealing, the reduction in Zn—O bonds does not completely explain the increase in contact resistance, although, a change in the structural properties can explain it. Fig. 3 shows the XRD patterns of as-deposited and annealed ZnO films. In the as-deposited films, the peaks related to the planes (100), (002), (101) and (110) can be identified, where the strongest peak is associated to the (100) plane. At 30 min of annealing, the peaks related to the planes (100), (002), (101), (102), (103), (112) and (110) can be identified, where the strongest peak is now associated to the (002) plane. The XRD diffractogram confirms a better polycrystalline nature of ZnO films for 30 min of annealing. The preferential orientation in (002) plane (in agreement with the JCPDS Card No. 36-1451) has been reported in high-performance ZnO films [42,44,45]. On the other hand, at 60 min of annealing, the preferential orientation in (002) plane is lost, moreover, the peaks related to the planes (102) and (103) are disappeared, indicating the loss of the polycrystalline nature of the film. In addition, a reduction in the peaks related to the planes (100) and (101) is appreciated for ZnO films annealed for 60 min compared to as-deposited films. These results confirm the degradation of the 60 min annealed ZnO film and are in agreement with the FTIR spectroscopy, conductivity and contact resistance results.

In order to demonstrate the above conclusions in the improvement of electrical characteristics of ZnO TFTs with Al electrodes, Fig. 4 shows





Fig. 2. FTIR spectra of as-deposited and annealed ZnO films.





Fig. 3. XRD patterns of as-deposited and annealed ZnO films.

the output characteristics of as-fabricated and annealed ZnO TFTs. The as-fabricated ZnO TFTs exhibit a super linear behavior and no saturation regime, these are related to a low quality Al-ZnO interface. This low-quality interface resulted by an  $Al_2O_3$  ultra-thin layer formed in the metal-semiconductor interface during the fabrication of the devices [46]. However, after 30 min of annealing at 180 °C, the output characteristics present a clear saturation regime. This is in agreement with the results previously demonstrated. An offset voltage can be observed in both output characteristics, the family curves start at approximately 2.5 V of Vds. This effect has been reported in other ZnO based TFTs [47].

The results shown that an improvement in the metal-ZnO contact can be possible at low annealing temperature with longer annealing time. Since there is an optimal annealing time and after this time, the metal-ZnO interface deteriorates. The optimal annealing time may be different for other metal contacts or deposition techniques. These results can be useful in applications such as Thin-film Transistors and solar cells.

#### 4. Conclusions

The study of annealing effects on Al-ZnO contacts is presented. For the first time, the results show that an improvement in the metal-ZnO contact can be possible at low annealing temperature with longer



Fig. 4. Output characteristics of ZnO thin-film transistors. a) as-fabricated, b) after 30 min of annealing.

annealing time. Since there is an optimal annealing time and after this time, the metal-ZnO interface deteriorates. This assumption was corroborated by XRD, where the results confirm an enhancement of the ZnO films polycrystalline structure after 30 min of annealing; and the degradation of the polycrystalline structure after 60 min of annealing. FTIR spectroscopy shows a reduction of Zn—O bonds as the annealing time is increased. The output characteristics of ZnO TFTs present a clear saturation regime after 30 min of annealing, demonstrating the conclusions previously made.

#### Acknowledgment

The authors want to thank Manuel Escobar of the Laboratory of Microelectronics at INAOE-Mexico for the assistance in the fabrication of the structures used in this work. M. Dominguez wants to thank NPTC-PRODEP by SEP-Mexico for the financial support to this project.

#### References

- Y. Noh, S. Na, S. Kim, Inverted polymer solar cells including ZnO electron transport layer fabricated by facile spray pyrolysis, Sol. Energy Mater. Sol. Cells 117 (2013) 139–144.
- [2] M. Krunks, A. Katerski, T. Dedova, I. Oja Acik, A. Mere, Nanostructured solar cell based on spray pyrolysis deposited ZnO nanorod array, Sol. Energy Mater. Sol. Cells 92 (2008) 1016–1019.
- [3] S. Gledhill, A. Grimm, N. Allsop, T. Koehler, C. Camus, M. Lux-Steiner, C. Fischer, A spray pyrolysis route to the undoped ZnO layer of Cu(In, Ga)(S, Se)2 solar cells, Thin Solid Films 517 (2009) 2309–2311.
- [4] M. Dominguez, F. Flores, A. Luna, S. Alcantara, J. Martinez, J. Luna, P. Rosales, C. Reyes, Annealing effects on metal-ZnO interface for improvement performance of Thin-film Transistors, Mater. Res. Soc. Symp. Proc. 1731 (2015) 03–01.
- [5] M. Dominguez, F. Flores, A. Luna, J. Martinez, J. Luna-Lopez, S. Alcantara, P. Rosales, C. Reyes, A. Orduña, Impact of active layer thickness in thin-film transistors based on Zinc Oxide by ultrasonic spray pyrolysis, Solid State Electron. 109 (2015) 33–36.
- [6] S. Oertel, M. Jank, E. Teuber, A. Bauer, L. Frey, High-mobility metal-oxide thin-film transistors by spray deposition of environmentally friendly precursors, Thin Solid Films 553 (2014) 114–117.
- [7] H. Morkoc, U. Ozgur, Zinc Oxide, Wiley-VCH, 2009.
- [8] H. Kim, S. Han, T. Seong, W. Choi, Low-resistance Ti/Au ohmic contacts to Al-doped ZnO layers, Appl. Phys. Lett. 77 (2000) 1647–1649.
- [9] H. Kim, S. Han, T. Seong, W. Choi, Electrical and structural properties of Ti/Au ohmic contacts to n-ZnO, J. Electrochem. Soc. 148 (2001) G114–G117.
- [10] V. Hoppe, D. Stachel, D. Beyer, The oxygen coordination of metal ions in phosphate and silicate glasses studied by a combination of X-ray and neutron diffraction, Phys. Scr. T57 (1995) 122–126.
- [11] H. Ishikawa, K. Tsukui, Y. Koide, N. Teraguchi, Y. Tomomura, A. Susuki, M. Murakami, Effects of surface cleaning on electrical properties for Ni contacts to p-type ZnSe, J. Vac. Sci. Technol. B 14 (1996) 1812–1818.
- [12] H. Sheng, N. Emanetoglu, S. Muthukumar, S. Feng, Y. Lu, Nonalloyed Al ohmic contacts to MgxZn1-xO, J. Electron. Mater. 31 (2002) 811–814.
- [13] A. Iliadis, R. Vispute, T. Venkatesan, K. Jones, Ohmic metallization technology for wide band-gap semiconductors, Thin Solid Films 420–421 (2002) 478–486.
- [14] J. Lee, K. Kim, S. Park, W. Choi, Low-resistance and nonalloyed ohmic contacts to plasma treated ZnO, Appl. Phys. Lett. 78 (2001) 3842–3844.
- [15] H. Kim, K. Kim, S. Park, T. Seong, Y. Yoon, Thermally stable and low resistance Ru ohmic contacts to n-ZnO, Jpn. J. Appl. Phys. 41 (2002) L546–L548.
- [16] T. Akane, K. Sugioka, K. Midorikawa, Nonalloy ohmic contact fabrication in a hydrothermally grown n-ZnO (0001) substrate by KrF excimer laser irradiation, J. Vac. Sci. Technol. B 18 (2000) 1406–1408.
- [17] K. Sekhar, S. Levichev, K. Kamakshi, S. Doyle, A. Chahboun, M. Gomes, Effect of rapid thermal annealing on texture and properties of pulsed laser deposited zinc oxide thin films, Mater. Lett. 98 (2013) 149–152.
- [18] T. Hiramatsu, M. Furuta, T. Matsuda, C. Li, T. Hirao, Behavior of oxygen in zinc oxide films through thermal annealing and its effect on sheet resistance, Appl. Surf. Sci. 257 (2011) 5480–5483.
- [19] A. Ahmad, A. Alsaad, B. Albiss, M. Akhras, H. Nasser, I. Qattan, Optical and structural properties of sputter deposited ZnO thin films in relevance to post-annealing and substrate temperatures, Thin Solid Films 606 (2016) 133–142.
- [20] H. Park, J. Jo, H. Hong, J. Heo, Influence of post-deposition annealing on the electrical properties of zinc oxide thin films, Thin Solid Films 573 (2014) 22–26.
- [21] J. Kennedy, P. Murmu, J. Leveneur, A. Markwitz, J. Futter, Controlling preferred orientation and electrical conductivity of zinc oxide thin films by postgrowth annealing treatment, Appl. Surf. Sci. 367 (2016) 52–58.
- [22] A. Kumar, T. Sarmash, D. Rani, L. Obulapathi, G. Rao, T. Rao, K. Asokan, Effect of post sputter annealing treatment on nano-structured cadmium zinc oxide thin films, J. Alloys Compd. 665 (2016) 86–92.
- [23] J. Won, Y. Han, H. Seol, K. Lee, R. Choi, J. Jeong, Transport property improvements of amorphous In-Zn-O transistors with printed Cu contacts via rapid temperature annealing, Thin Solid Films 603 (2016) 268–271.

- [24] Z. Yuan, X. Zhu, X. Wang, X. Cai, B. Zhang, D. Qiu, H. Wu, Annealing effects of In<sub>2</sub>O<sub>3</sub> thin films on electrical properties and application in thin film transistors, Thin Solid Films 519 (2011) 3254–3258.
- [25] M. Kim, H. Lee, Effect of In addition and annealing temperature on the device performance of solution-processed In-Zn-Sn-O thin film transistors, Solid State Electron. 96 (2014) 14–18.
- [26] B. Li, H. Wang, D. Zhou, Z. Hu, H. Wu, Y. Peng, L. Yi, X. Zhang, Y. Wang, Effect of annealing temperature on the electrical properties of In-Zn-Li-O thin film transistors, Solid State Electron. 111 (2015) 18–21.
- [27] H. Tang, K. Ide, H. Hiramatsu, S. Ueda, N. Ohashi, H. Kumomi, H. Osono, T. Kamiya, Effects of thermal annealing on elimination of deep defects in amorphous In-Ga-Zn-O thin film transistors, Thin Solid Films 614 (2016) 73–78.
- [28] C. Ahn, S. Kim, Y. Kim, H. Lee, H. Cho, Effect of post-annealing temperatures on thin-film transistors with ZnO/Al2O3 superlattice channels, Thin Solid Films 584 (2015) 336–340.
- [29] L. Lu, J. Li, M. Wong, A comparative study on the effects of annealing on the characteristics of zinc oxide thin-film transistors with gate-stacks of different gas-permeability, IEEE Electron Device Lett. 35 (2014) 841–843.
- [30] J. Jin, Y. Luo, P. Bao, C. Brox, R. Potter, A. Song, Tuning the electrical properties of ZnO thin-film transistors bt thermal annealing in different gases, Thin Solid Films 552 (2014) 192–195.
- [31] L. Lu, M. Wong, The resistivity of zinc oxide under different annealing configurations and its impact on the leakage characteristics of zinc oxide thin-film transistors, IEEE Trans. Electron Devices 61 (2014) 1077.
- [32] M. Dominguez, P. Rosales, A. Torres, F. Flores, J. Molina, M. Moreno, J. Luna, A. Orduña, Effects of germane flow rate in electrical properties of a-SiGe:H films for ambipolar thin-film transistors, Thin Solid Films 562 (2014) 260.
- [33] C.R. Kagan, P. Andry, Thin Film Transistors, Marcel Dekker Inc., 2003
- [34] E. Lueder, Liquid Crystal Displays, John Wiley & Sons, 2001.
- [35] R. Street, Thin-film transistors, Adv. Mater. 21 (2009) 2007.
- [36] D. Schroder, Semiconductor Material and Device Characterization, Wiley, New York, 1998.

- [37] K. Nomura, T. Kamiya, H. Hosono, Ambipolar oxide thin-film transistor, Adv. Mater. 23 (2011) 3431–3434.
- [38] J. Chung, J. Lee, H. Kim, N. Jang, J. Kim, Effect of thickness of ZnO active layer on ZnO-TFT's characteristics, Thin Solid Films 516 (2008) 5597–5601.
- [39] P. Nunes, A. Malik, B. Fernandes, E. Fortunato, P. Vilarinho, R. Martins, Influence of the doping and annealing atmosphere on zinc oxide thin films deposited by spray pyrolysis, Vacuum 52 (1999) 45–49.
- [40] P. Nunes, E. Fortunato, R. Martins, Influence of the annealing conditions on the properties of ZnO thin films, Int. J. Inorg. Mater. 3 (2001) 1125–1128.
- [41] P. Nunes, B. Fernandes, E. Fortunato, P. Vilarinho, R. Martins, Performances presented by zinc oxide thin films deposited by spray pyrolysis, Thin Solid Films 337 (1999) 176–179.
- [42] G. Adamopoulos, A. Bashir, W. Gillin, S. Georgakopoulos, M. Shkunov, M. Baklar, N. Stingelin, D. Bradley, T. Anthopoulos, Structural and electrical characterization of ZnO films grown by spray pyrolysis and their application in thin- film transistors, Adv. Funct. Mater. 21 (2011) 525–531.
- [43] R. Gayen, K. Sarkar, S. Hussain, R. Bhar, A. Pal, Zno films prepared by modified solgel technique, Indian J. Pure Appl. Phys. 49 (2011) 470–477.
- [44] J. Lee, B. Park, Characteristics of Al-doped ZnO thin films obtained by ultrasonic spray pyrolysis: effects of Al doping and an annealing treatment, Mater. Sci. Eng. B 106 (2004) 242–245.
- [45] S. Benramache, B. Benhaoua, Influence of annealing temperature on structural and optical properties of ZnO: In thin films prepared by ultrasonic spray technique, Superlattice. Microst. 52 (2012) 1062–1070.
- [46] M. Dominguez, F. Flores, A. Luna, J. Martinez, J. Luna-Lopez, S. Alcantara, P. Rosales, C. Reyes, Thin-film transistors based on zinc oxide films by ultrasonic spray pyrolysis, Rev. Mex. Fis. 61 (2015) 123–126.
- [47] Y. Jeong, C. Pearson, Y. Lee, L. Winchester, J. Hwang, H. Kim, L. Do, M. Petty, Zinc oxide thin-film transistors fabricated at low temperature by chemical spray pyrolysis, J. Electron. Mater. 43 (2014) 4241–4245.